Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10260	((cache or cached or caching) with data) and (SAN or storage adj area adj network or DASD or storage adj network)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:41
L2	2	((circuit adj board) near5 (remove or removed or removing)) with (cache or cahed or caching)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:46
L3	25029125	@ad<"20040206"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:45
L4	1	2 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:46
L5	3066	(erase or delete or remove) with cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:46
L6	690	1 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:46
L7	9819	(circuit adj board) near5 (remove or removed or removing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:47
L8	1	6 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:47
L9	949	711/133.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:48
L10	759	711/111.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:47
L11	1541	711/112.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:47

L12	3037	9 or 10 or 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:47
L13	66	6 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:47
L14	63	3 and 13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:48
L15	63	5 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:48
L16	0	7 and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 19:48
L17	307	711/133.ccls.	US-PGPUB	OR	ON	2005/11/09 19:48
L18	263	3 and 17	US-PGPUB	OR	ON	2005/11/09 19:48
L19	0	7 and 18	US-PGPUB	OR	ON	2005/11/09 19:49
L20	57	1 and 18	US-PGPUB	OR	ON	2005/11/09 19:49
L21	21	5 and 20	US-PGPUB	OR	ON	2005/11/09 19:49

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☐   1. **Design trade-offs in high-throughput coherence controllers**
Nguyen, A.-T.; Torrellas, J.;
Parallel Architectures and Compilation Techniques, 2003. PACT 2003. Proceedings. 12th International Conference on
27 Sept.-1 Oct. 2003 Page(s):194 - 205
Digital Object Identifier 10.1109/PACT.2003.1238015

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☐   1. **ASIC design of fast IP-lookup for next generation IP router**
Yuan-Sun Chu; Po-Feng Lin; Jia-Huang Lin; Hui-Kai Su; Ming-Jen Chen;
Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on
23-26 May 2005 Page(s):3825 - 3828 Vol. 4
Digital Object Identifier 10.1109/ISCAS.2005.1465464

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